

FIG. 1 (PRIOR ART)

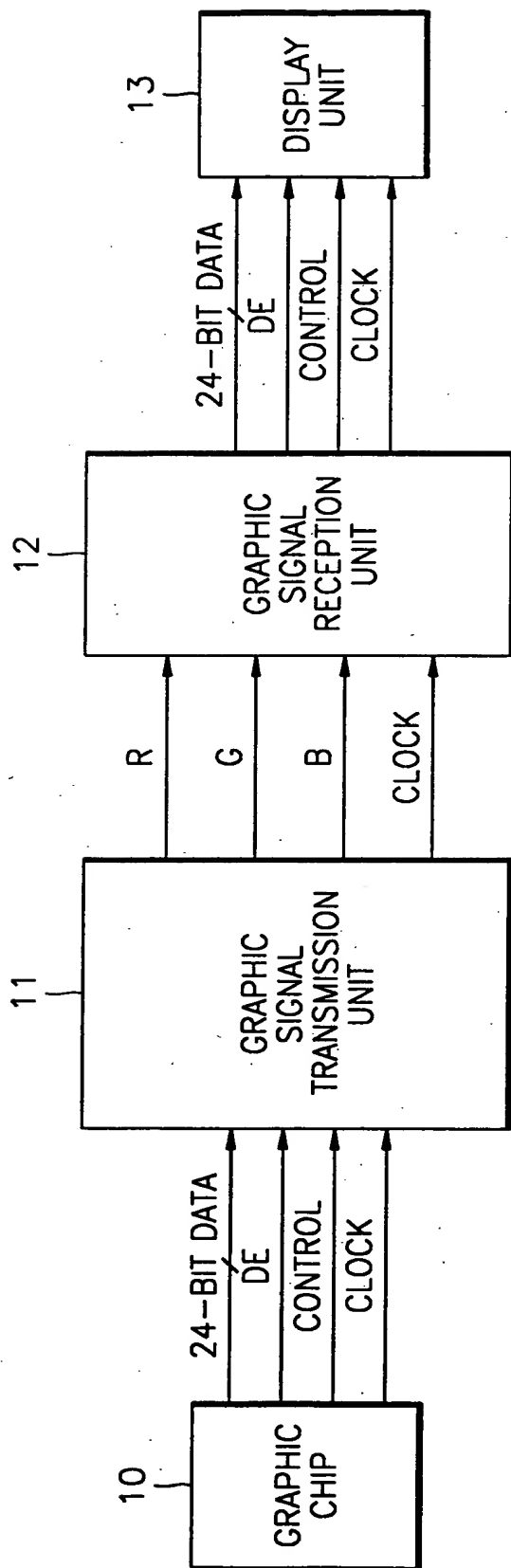


FIG. 2

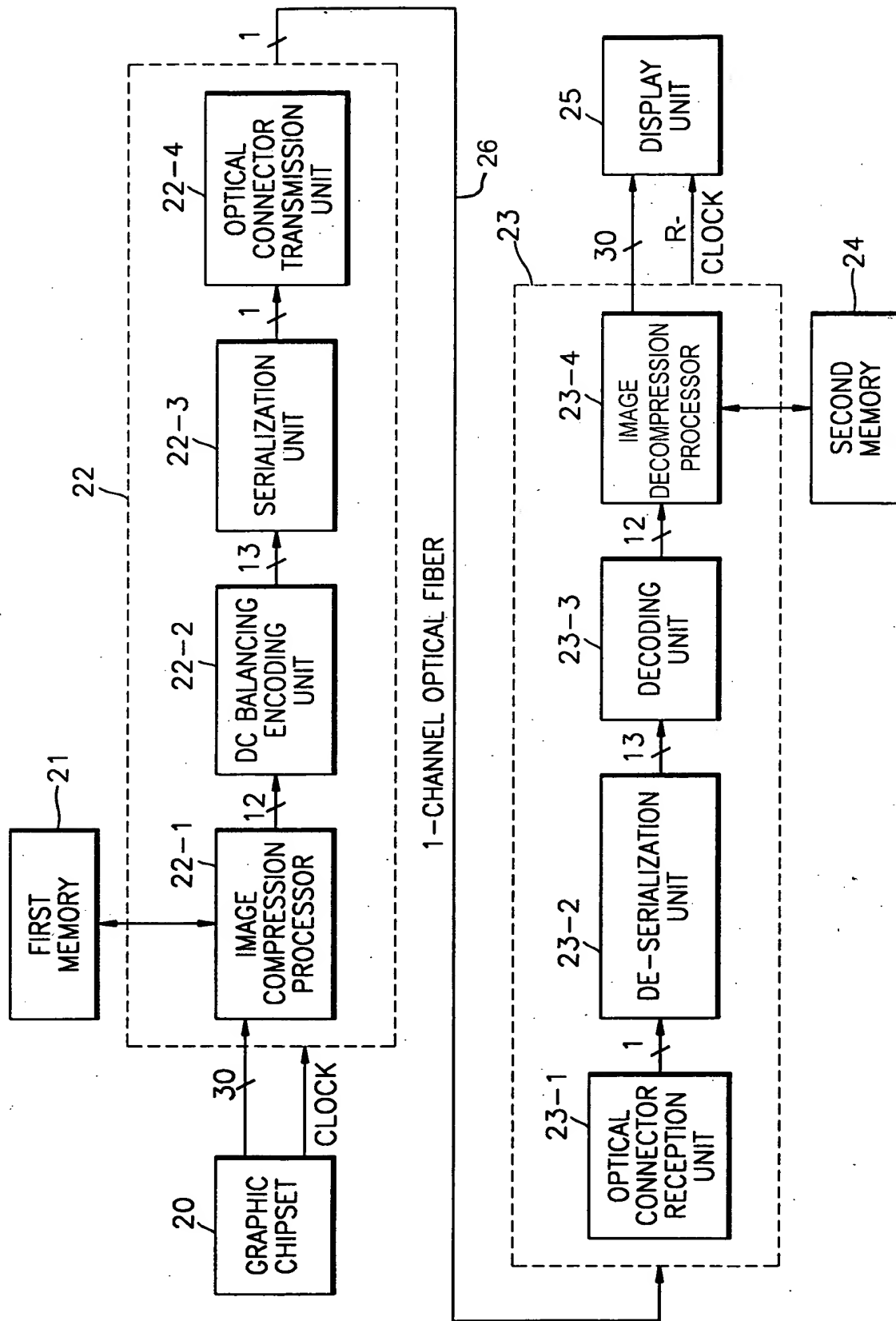


FIG. 3

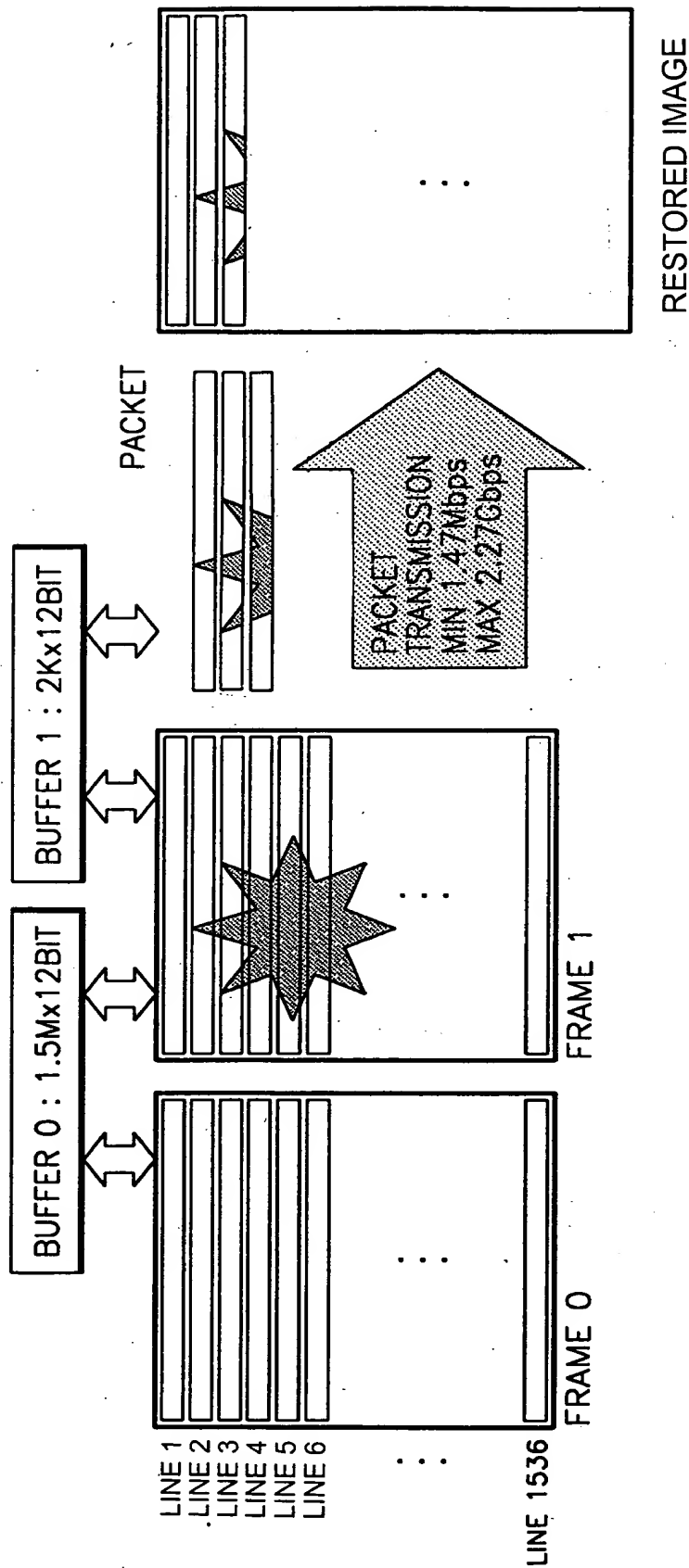


FIG. 4

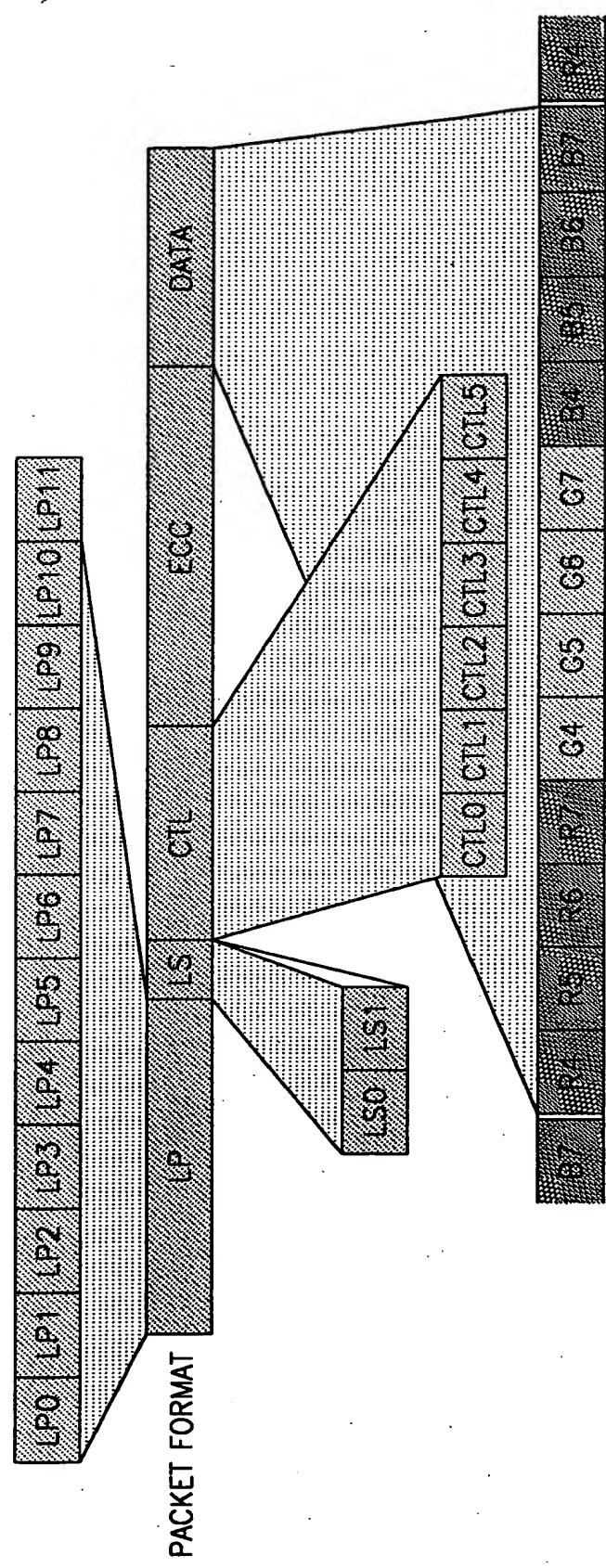
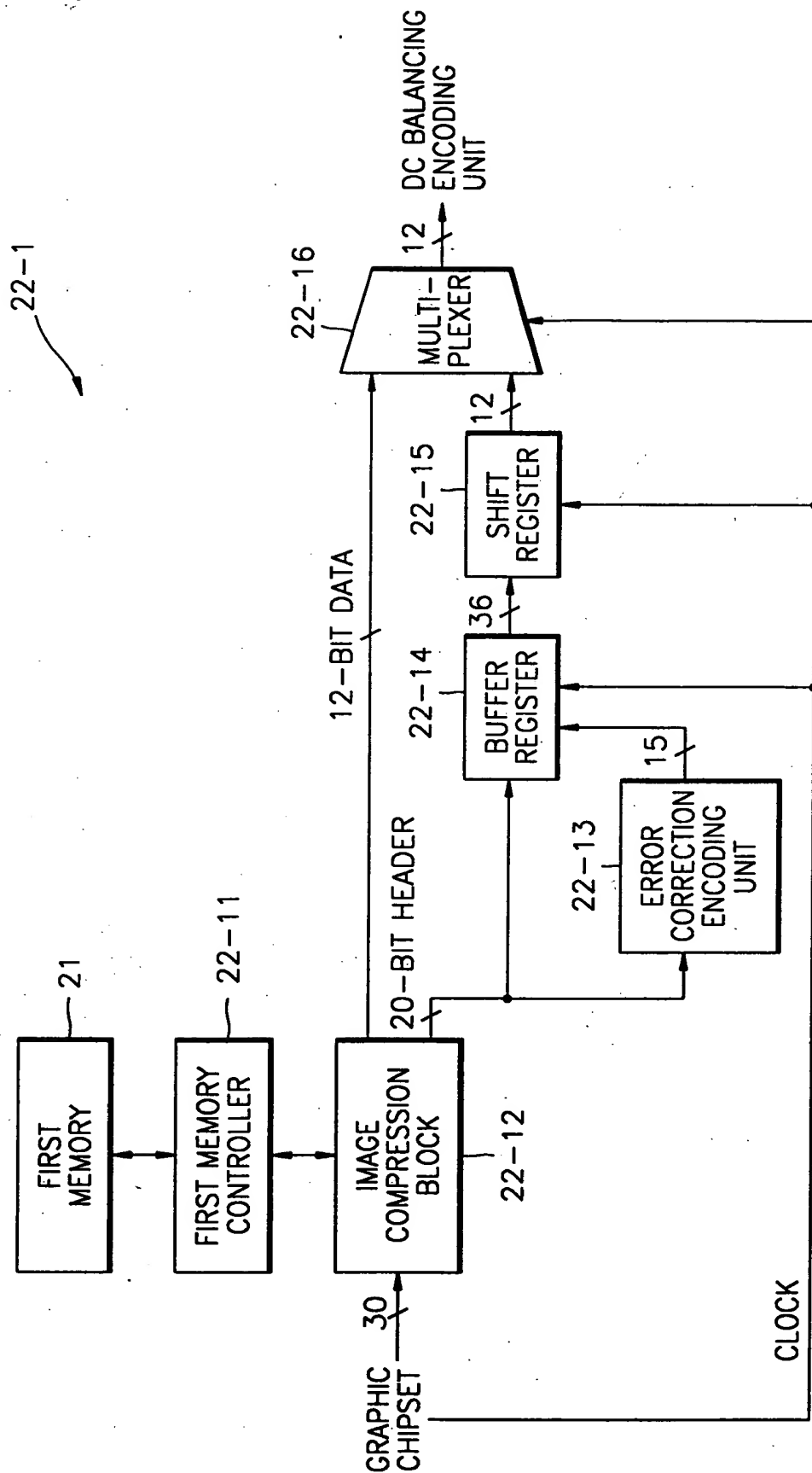
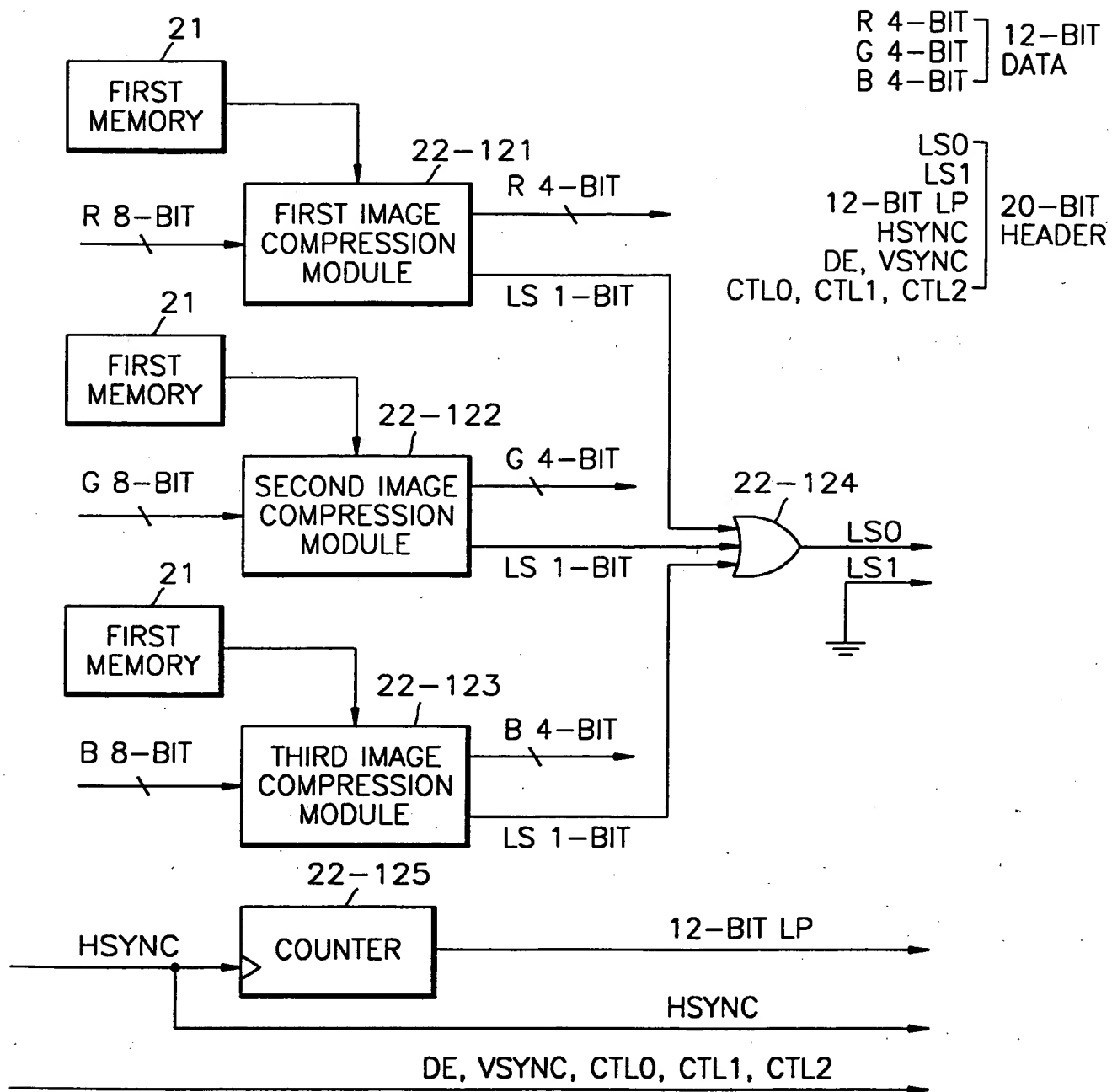


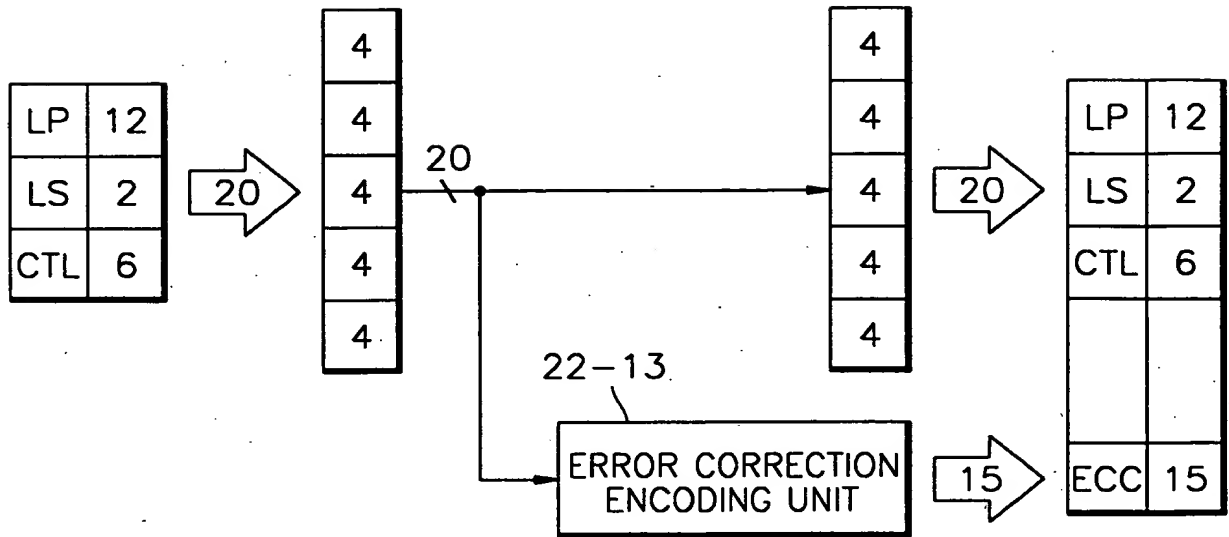
FIG. 5



# FIG. 6



# FIG. 7A



# FIG. 7B

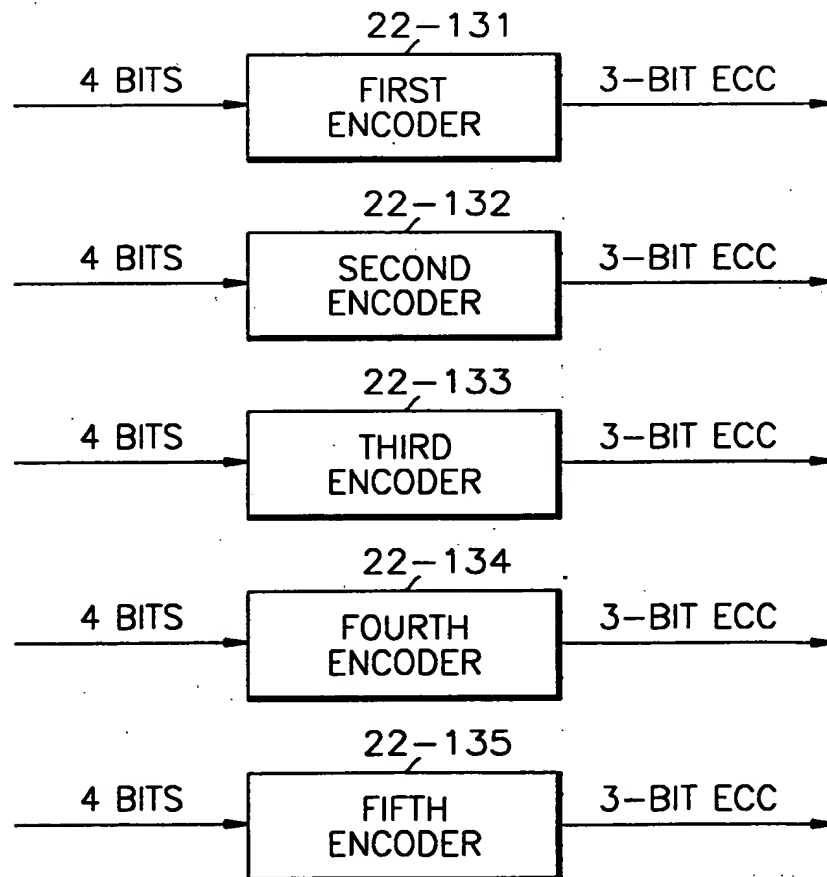


FIG. 8

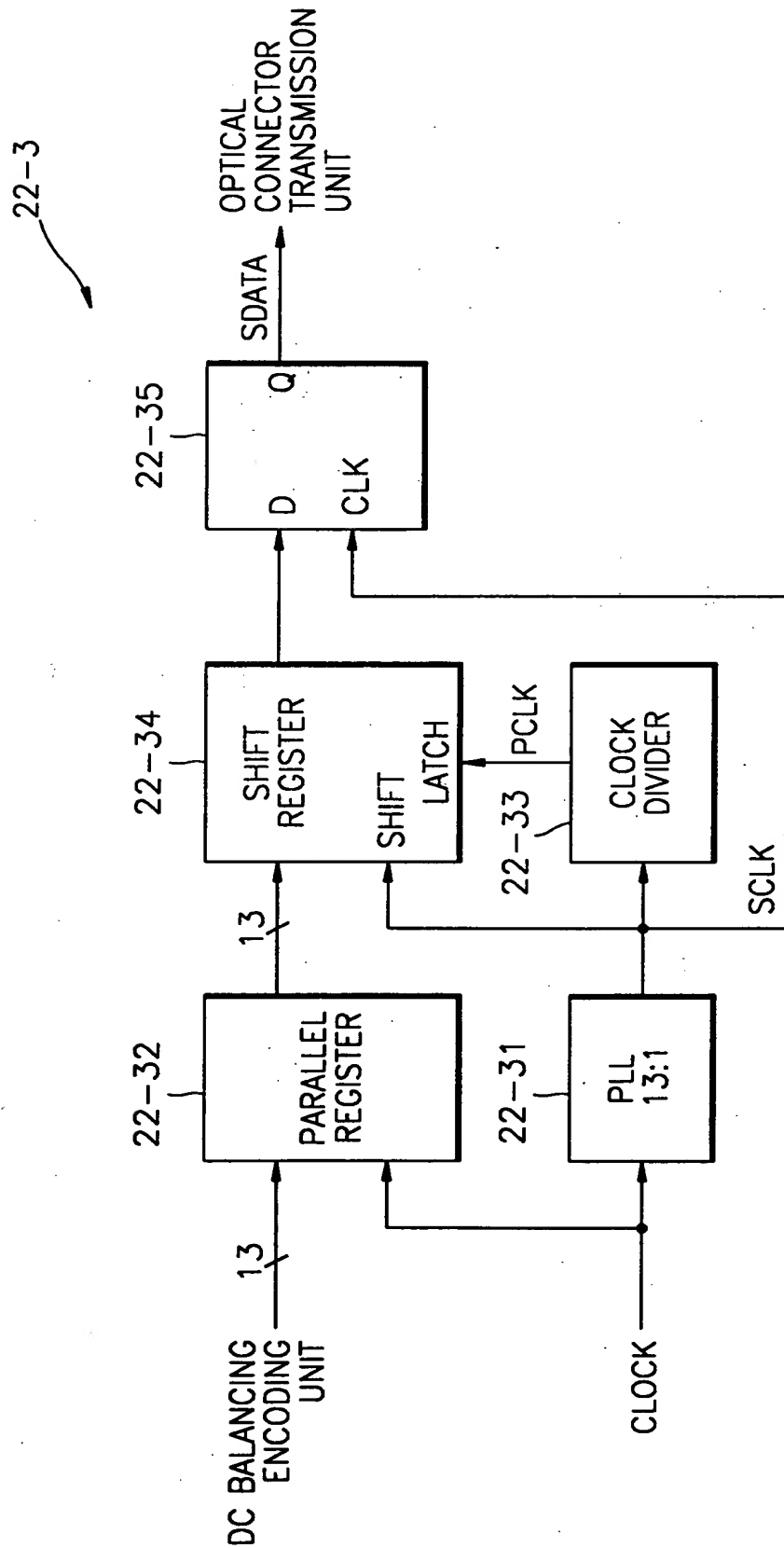




FIG. 9

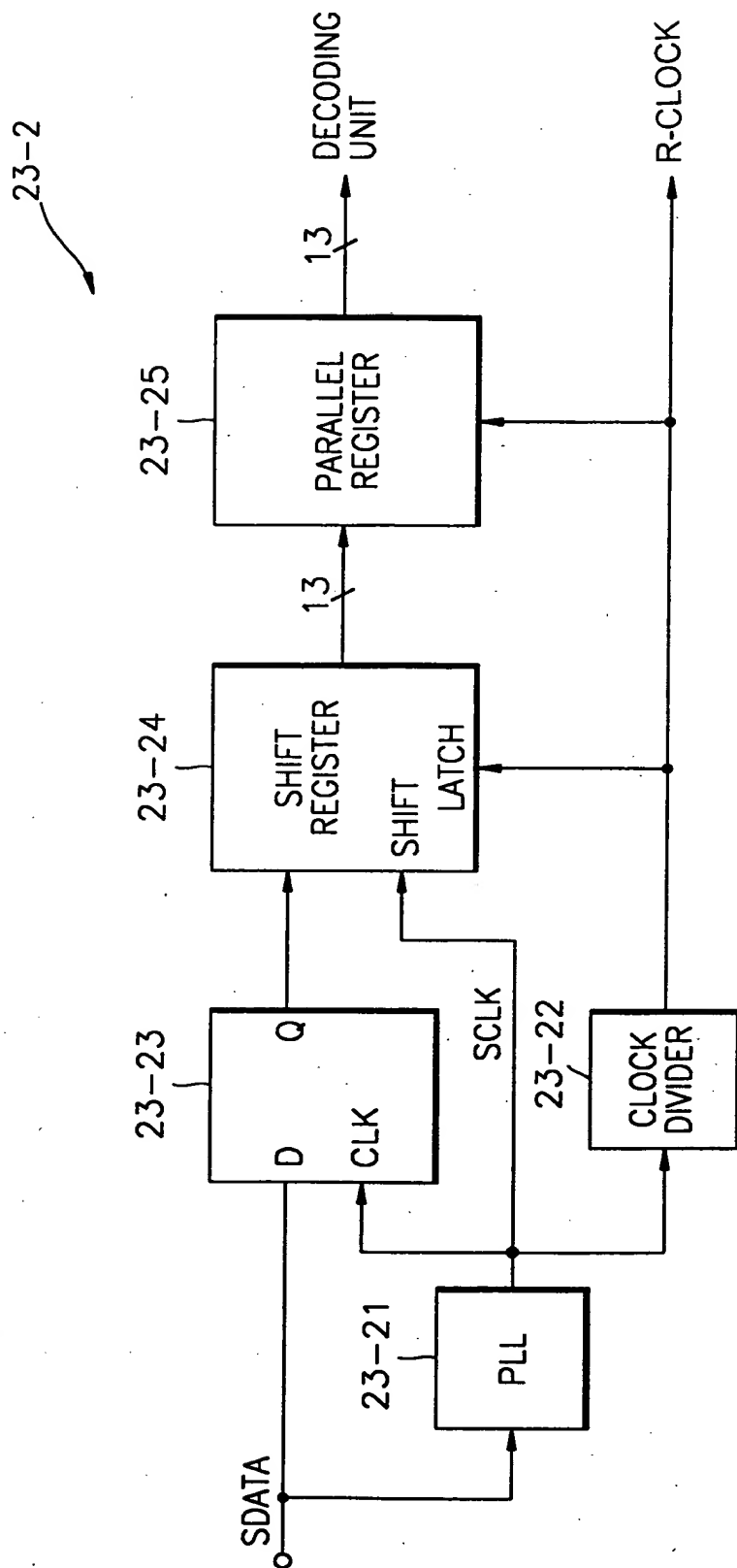
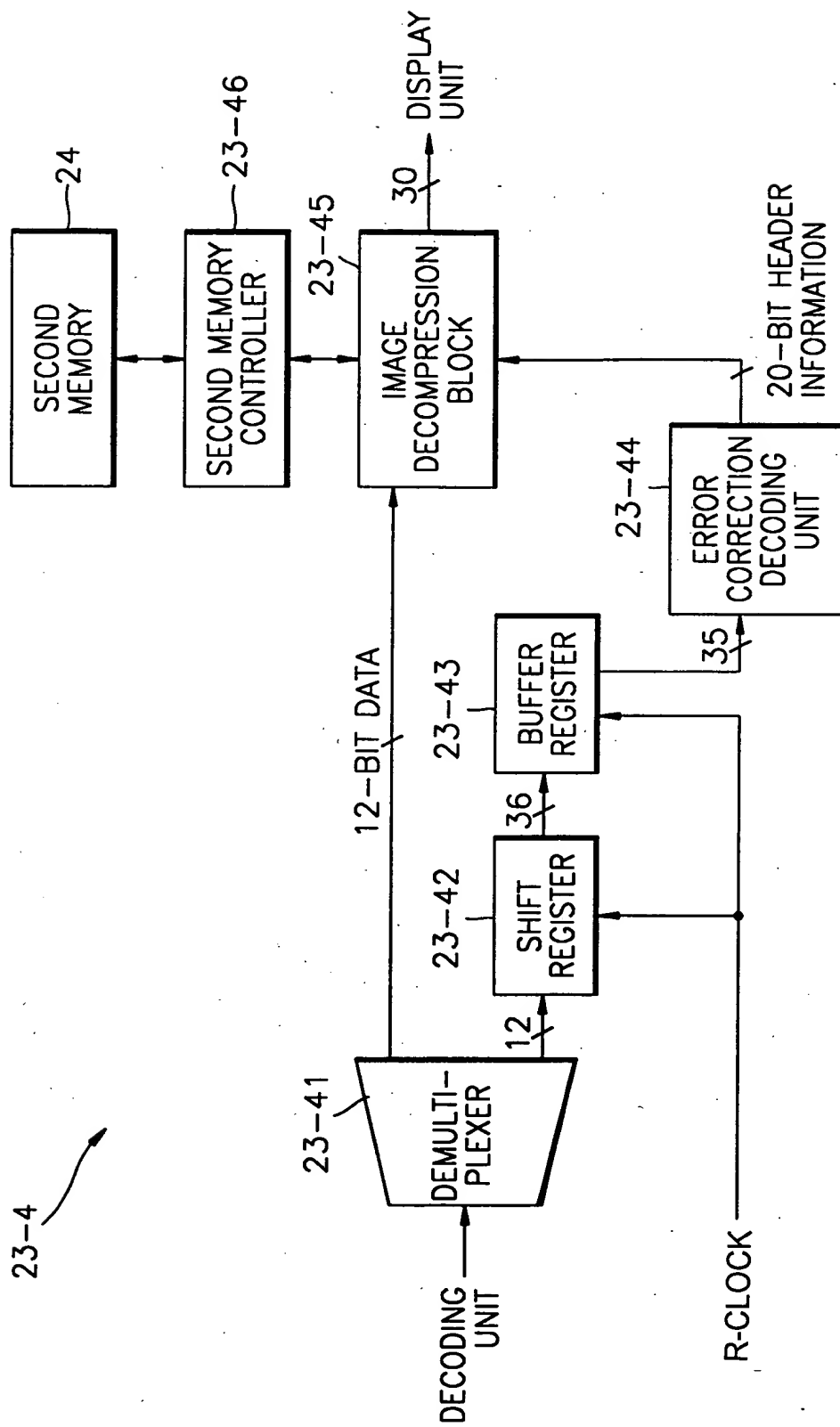
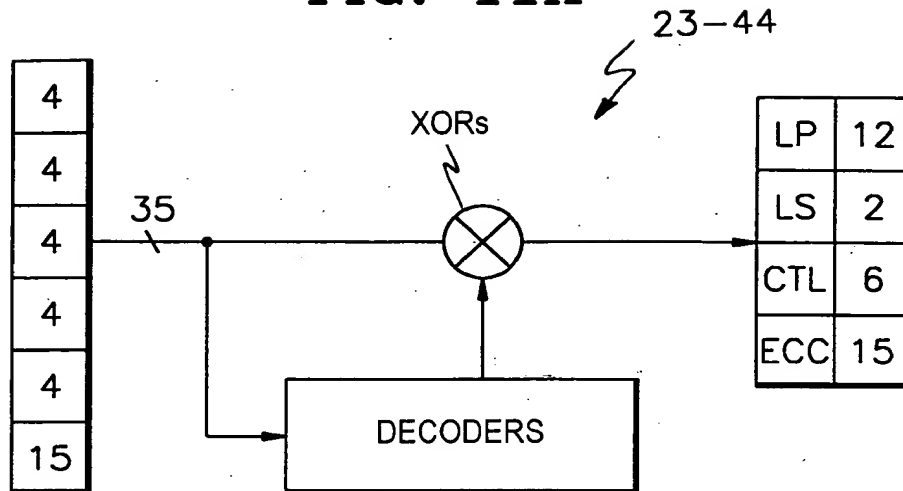


FIG. 10



# FIG. 11A



# FIG. 11B

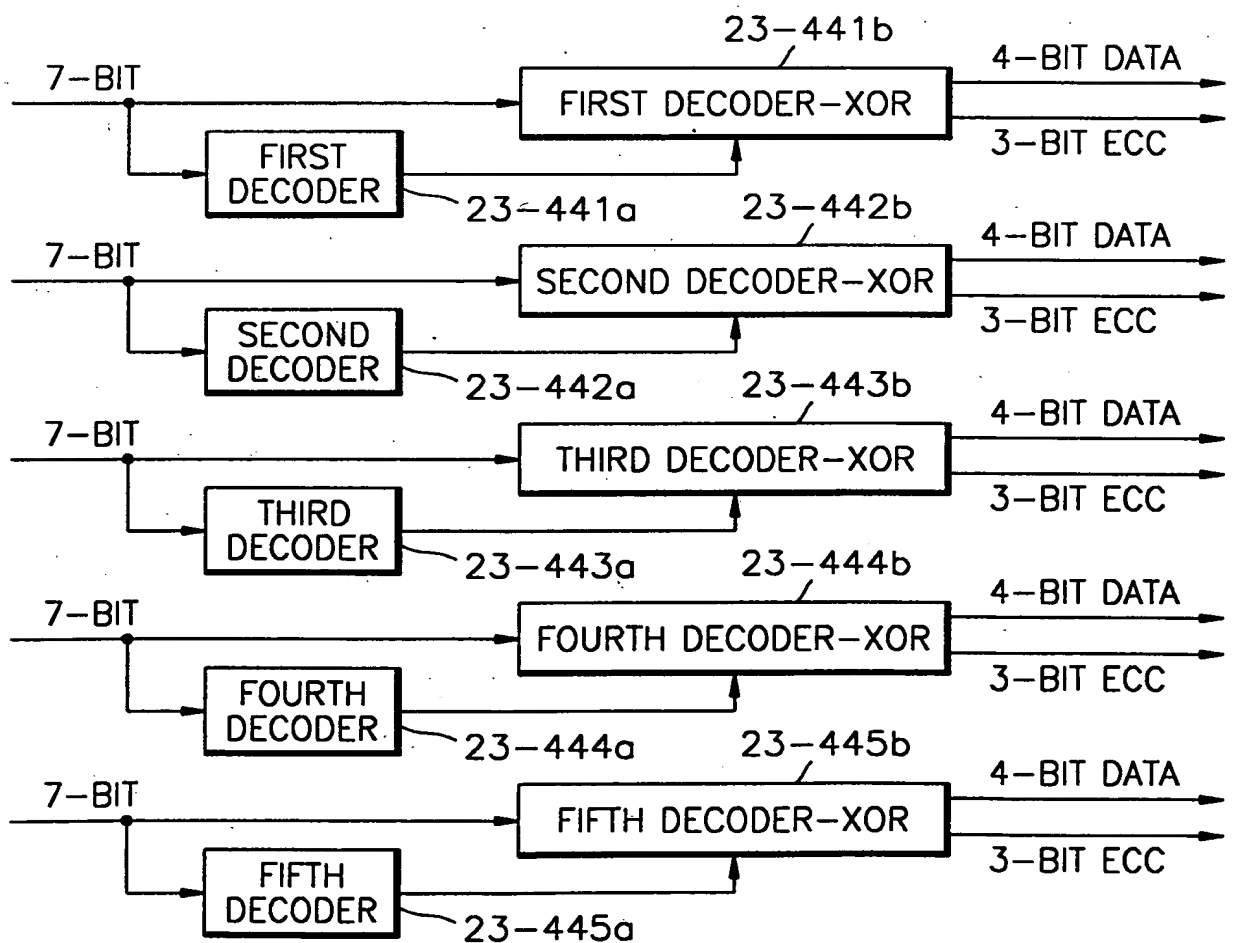


FIG. 12

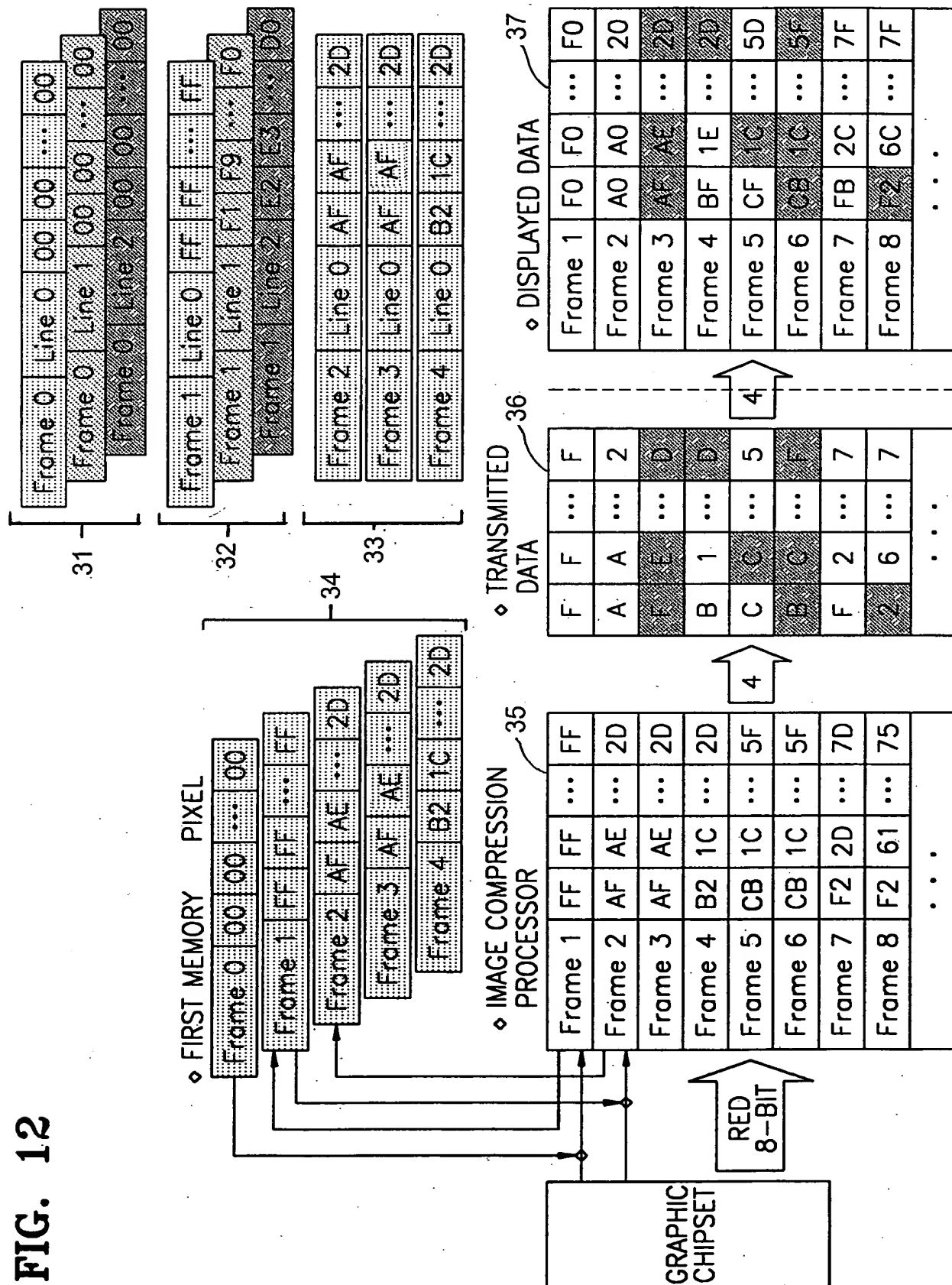


FIG. 13

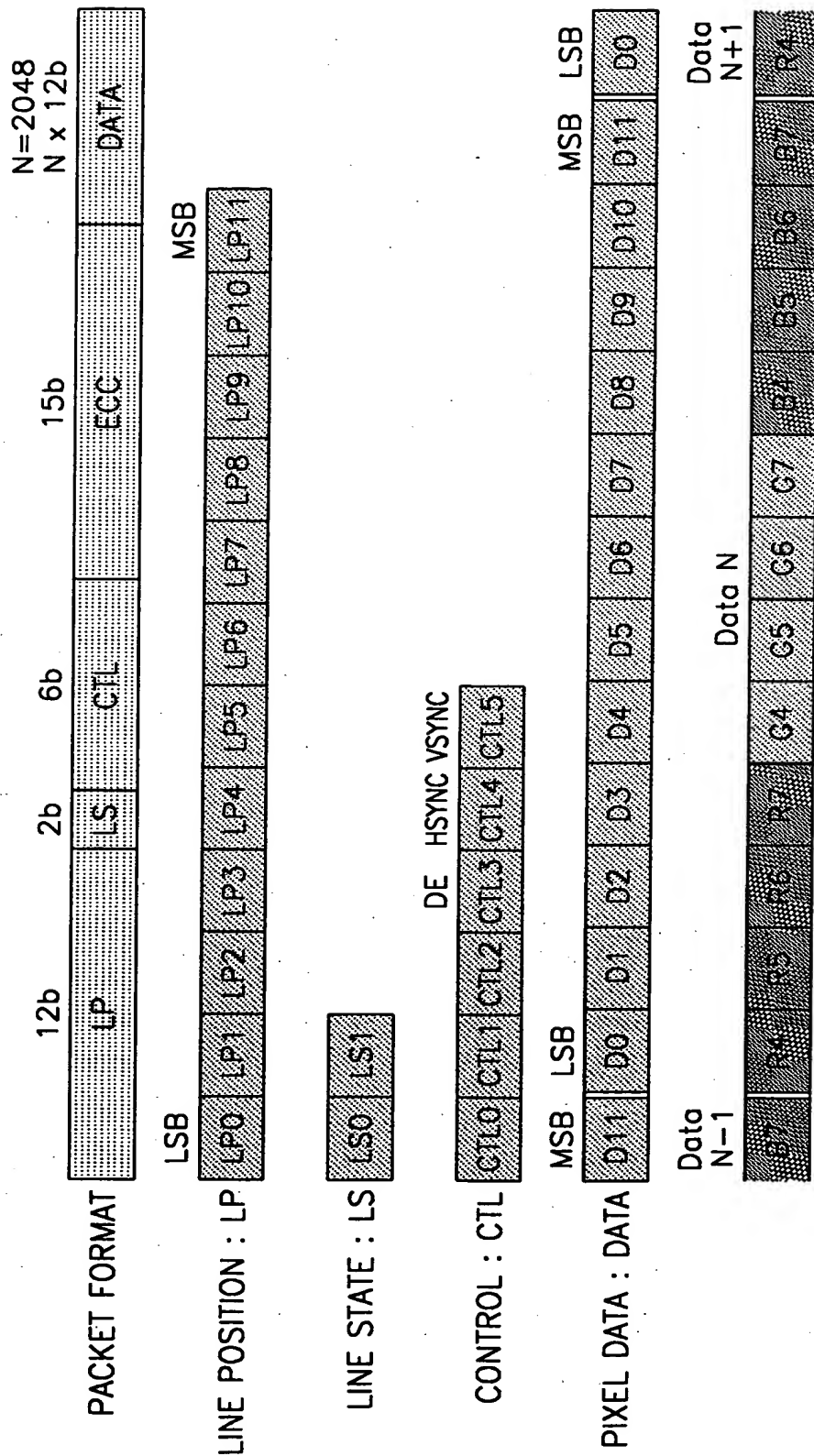


FIG. 14

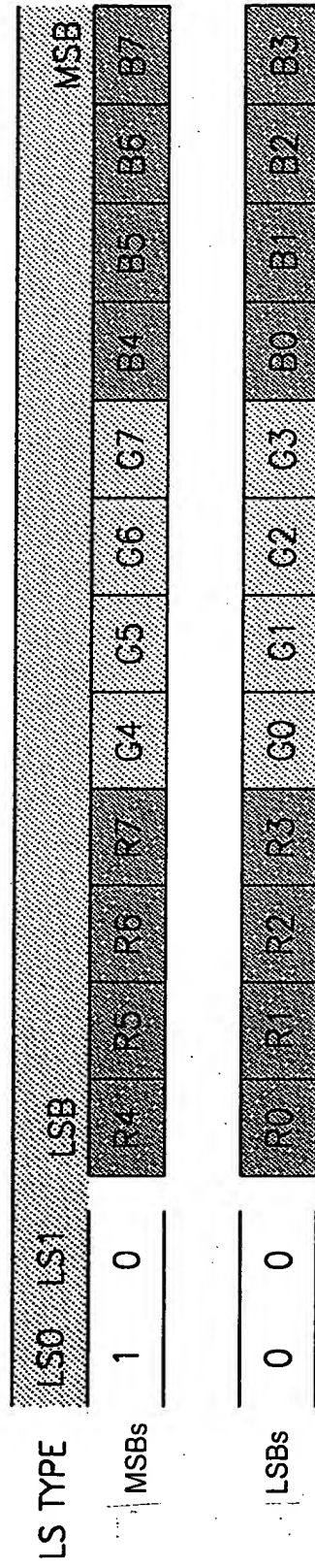


FIG. 15

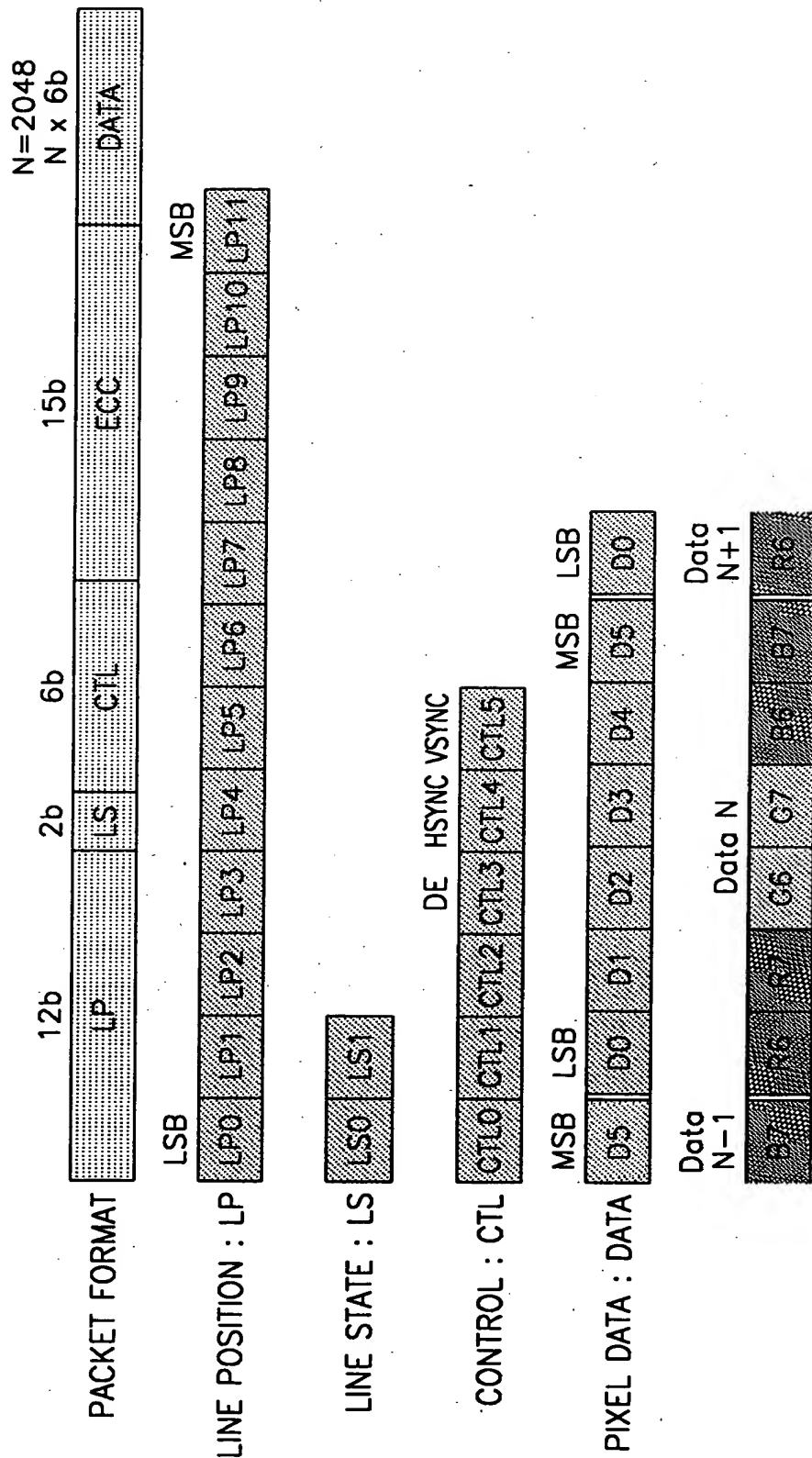


FIG. 16

LS TYPE	LS1		LSB							MSB		
	LS0											
UPPER MSBs	1	1										
			R6	R7	G6	G7	B6	B7				
LOWER MSBs	0	1										
			R4	R5	G4	G5	B4	B5				
UPPER LSBs	1	0										
			R2	R3	G2	G3	B2	B3				
LOWER LSBs	0	0										
			R0	R1	G0	G1	B0	B1				



FIG. 17

Frame Rate : 60 Hz 24-bit-Color

	Pixel	Line	Pixel	Line	MHz	2.5% Gbps/ch	4ch	1ch	1ch	1ch
						Gbps	Gbps	Gbps	Gbps	Gbps
SVGA	1056	628	800	600	40	0.40	1.20	578	0.35	
XGA	1344	806	1024	768	65	0.65	1.95	739	0.57	
SXGA	1696	1066	1280	1024	108	1.08	2.43	924	0.95	
UXGA	2160	1250	1600	1200	162	1.62	3.54	1,154	1.39	
HDTV	2592	1124	1920	1080	175	1.75	3.84	1,385	1.50	
QXGA	2768	1598	2048	1536	265	2.65	5.79	1,477	2.27	

TMD5 : Transition Minimized Differential Signal

GVIF : Gigabit Video Interface

HSGL-4L : 4-bit Packet Line (PRESENT INVENTION)